

# Complementary Plastic Silicon Power Transistors MJE170G, MJE171G, MJE172G (PNP), MJE180G, MJE181G, MJE182G (NPN)

The MJE170/180 series is designed for low power audio amplifier and low current, high speed switching applications.

### **Features**

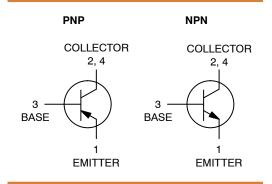
- High DC Current Gain
- High Current-Gain Bandwidth Product
- Annular Construction for Low Leakages
- Epoxy Meets UL 94 V-0 @ 0.125 in
- These Devices are Pb-Free and are RoHS Compliant\*

# **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-Base Voltage MJE170G, MJE180G MJE171G, MJE181G MJE172G, MJE182G	V <sub>CB</sub>	60 80 100	Vdc
Collector-Emitter Voltage MJE170G, MJE180G MJE171G, MJE181G MJE172G, MJE182G	V <sub>CEO</sub>	40 60 80	Vdc
Emitter-Base Voltage	V <sub>EB</sub>	7.0	Vdc
Collector Current - Continuous	I <sub>C</sub>	3.0	Adc
Collector Current - Peak	I <sub>CM</sub>	6.0	Adc
Base Current	I <sub>B</sub>	1.0	Adc
Total Power Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	12.5 0.012	W W/°C
Total Power Dissipation  @ T <sub>A</sub> = 25°C  Derate above 25°C	P <sub>D</sub>	1.5 0.1	W W/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C
ESD - Human Body Model	HBM	3B	V
ESD - Machine Model	MM	С	V

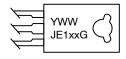
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

# 3 AMPERES POWER TRANSISTORS COMPLEMENTARY SILICON 40 - 60 - 80 VOLTS 12.5 WATTS





# **MARKING DIAGRAM**



Y = Year WW = Work Week

JE1xx = Specific Device Code

x = 70, 71, 72, 80, 81, or 82

G = Pb-Free Package

### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

1

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	10	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$	83.4	°C/W

# **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				<u> </u>
Collector–Emitter Sustaining Voltage (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = 0) MJE170G, MJE180G MJE171G, MJE181G MJE172G, MJE182G	V <sub>CEO</sub> (sus)	40 60 80	- - -	Vdc
Collector Cutoff Current	Ісво	- - -	0.1 0.1 0.1	μAdc mAdc
(V <sub>CB</sub> = 30 Vdc, 1 <sub>E</sub> = 0, 1 <sub>C</sub> = 130 O) MJE171G, MJE181G (V <sub>CB</sub> = 100 Vdc, 1 <sub>E</sub> = 0, T <sub>C</sub> = 150°C) MJE172G, MJE182G		-	0.1 0.1	
Emitter Cutoff Current (V <sub>BE</sub> = 7.0 Vdc, I <sub>C</sub> = 0)	I <sub>EBO</sub>	-	0.1	μAdc
ON CHARACTERISTICS				
DC Current Gain	h <sub>FE</sub>	50 30 12	250 - -	-
Collector–Emitter Saturation Voltage ( $I_C$ = 500 mAdc, $I_B$ = 50 mAdc) ( $I_C$ = 1.5 Adc, $I_B$ = 150 mAdc) ( $I_C$ = 3.0 Adc, $I_B$ = 600 mAdc)	V <sub>CE</sub> (sat)	- - -	0.3 0.9 1.7	Vdc
Base–Emitter Saturation Voltage ( $I_C = 1.5 \text{ Adc}, I_B = 150 \text{ mAdc}$ ) ( $I_C = 3.0 \text{ Adc}, I_B = 600 \text{ mAdc}$ )	V <sub>BE(sat)</sub>	- -	1.5 2.0	Vdc
Base-Emitter On Voltage (I <sub>C</sub> = 500 mAdc, V <sub>CE</sub> = 1.0 Vdc)	V <sub>BE(on)</sub>	-	1.2	Vdc
DYNAMIC CHARACTERISTICS				
Current-Gain - Bandwidth Product (Note 1) (I <sub>C</sub> = 100 mAdc, V <sub>CE</sub> = 10 Vdc, f <sub>test</sub> = 10 MHz)	f <sub>T</sub>	50	-	MHz
Output Capacitance $(V_{CB} = 10 \text{ Vdc}, I_E = 0, f = 0.1 \text{ MHz})$ MJE171G/MJE172G MJE181G/MJE182G	C <sub>ob</sub>	- -	60 40	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1.  $f_T = |h_{fe}| \cdot f_{test}$ .

<sup>1 1 101 1031</sup> 

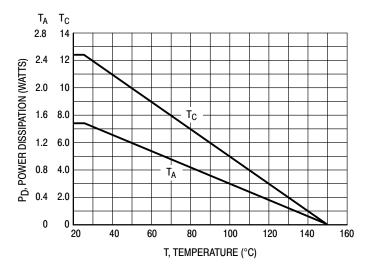
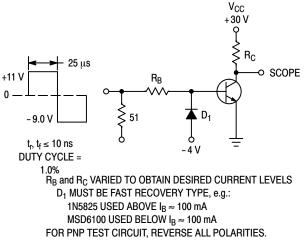


Figure 1. Power Derating

1K

500

300 200



T<sub>J</sub> = 25°C

T<sub>J</sub> = 25°C

T<sub>J</sub> = 25°C

NPN MJE181/182

PNP MJE171/172

1
0.01 0.02 0.03 0.05 0.1 0.2 0.3 0.5 1 2 3 5

I<sub>C</sub>, COLLECTOR CURRENT (AMPS)

Figure 2. Switching Time Test Circuit

Figure 3. Turn-On Time

V<sub>CE</sub> = 30 V

 $I_C/I_B = 10$ 

 $V_{BE(off)} = 4.0 V$ 

10

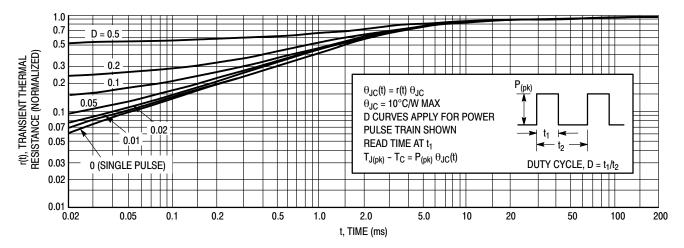


Figure 4. Thermal Response

# **ACTIVE-REGION SAFE OPERATING AREA**

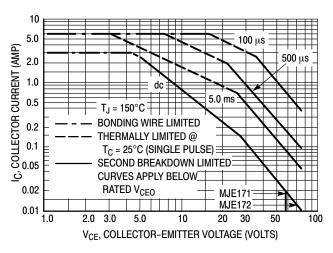


Figure 5. MJE171, MJE172

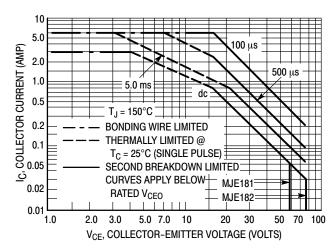


Figure 6. MJE181, MJE182

There are two limitations on the power handling ability of a transistor – average junction temperature and second breakdown. Safe operating area curves indicate  $I_C$  –  $V_{CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figures 5 and 6 is based on  $T_{J(pk)} = 150^{\circ} C$ ;  $T_{C}$  is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided  $T_{J(pk)} < 150^{\circ} C$ .  $T_{J(pk)}$  may be calculated from the data in Figure 4. At high case temperature, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

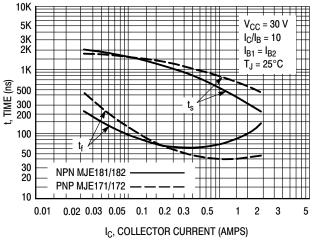


Figure 7. Turn-Off Time

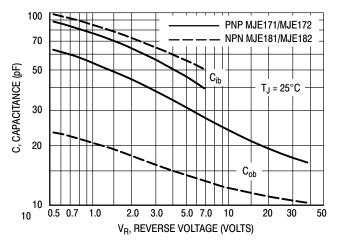
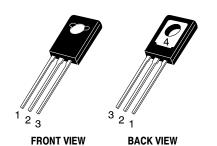


Figure 8. Capacitance

# **ORDERING INFORMATION**

Device	Package	Shipping
MJE170G	TO-225 (Pb-Free)	500 Units / Box
MJE171G	TO-225 (Pb-Free)	500 Units / Box
MJE172G	TO-225 (Pb-Free)	500 Units / Box
MJE180G	TO-225 (Pb-Free)	500 Units / Box
MJE181G	TO-225 (Pb-Free)	500 Units / Box
MJE182G	TO-225 (Pb-Free)	500 Units / Box

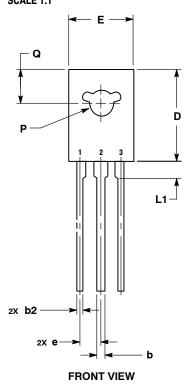


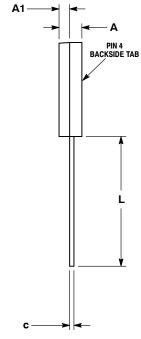


TO-225 CASE 77-09 **ISSUE AD** 

**DATE 25 MAR 2015** 

# SCALE 1:1



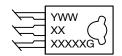


**SIDE VIEW** 

- NOTES: 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: MILLIMETERS. 3. NUMBER AND SHAPE OF LUGS OPTIONAL.

	MILLIMETERS				
DIM	MIN	MAX			
Α	2.40	3.00			
A1	1.00	1.50			
b	0.60	0.90			
b2	0.51	0.88			
С	0.39	0.63			
D	10.60	11.10			
E	7.40	7.80			
е	2.04	2.54			
L	14.50	16.63			
L1	1.27	2.54			
P	2.90	3.30			
Q	3.80	4.20			

# **GENERIC MARKING DIAGRAM\***



= Year ww

= Work Week XXXXX = Device Code = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

,	EMITTER COLLECTOR	2., 4.	CATHODE ANODE		COLLECTOR	2., 4.	ANODE 1 ANODE 2		MT 2
STYLE 6:	BASE	STYLE 7:	GATE	3. STYLE 8:	EMITTER	STYLE 9:	GATE	3. STYLE 10:	GATE
	CATHODE	PIN 1.			SOURCE	PIN 1.			SOURCE
	GATE ANODE	,	GATE MT 2		GATE DRAIN	2., 4.	DRAIN	,	DRAIN GATE

DOCUMENT NUMBER:	98ASB42049B	Electronic versions are uncontrolled except when accessed directly from the Document Reposite Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.			
DESCRIPTION:	TO-225		PAGE 1 OF 1		

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi nakes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

onsemi, Onsemi, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase

#### ADDITIONAL INFORMATION

**TECHNICAL PUBLICATIONS:** 

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$ 

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales